

Case Docket Number: 61282-044

Customer Number: 20277

**UTILITY PATENT APPLICATION  
UNDER 37 CFR 1.53(b)**

Mail Stop Utility Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

22581 U.S. PTO  
10/697650  
  
103103

Transmitted herewith for filing is the patent application of:

INVENTOR: Shinichi Abe  
FOR: SEMICONDUCTOR INTEGRATED CIRCUIT AND INTERRUPT REQUEST  
OUTPUT METHOD THEREOF

Enclosed are:

- 19 pages of specification, claims, abstract.  
 Declaration and Power of Attorney.  
 Priority Claimed. **Japanese Patent Application No. JP 2002-320215**  
 Certified copy of \_\_\_\_\_  
 5 sheets of formal drawing.  
 An assignment of the invention to \_\_\_\_\_  
and the assignment recordation fee.  
 An associate power of attorney.  
 Information Disclosure Statement, Form PTO-1449 and reference.  
 Return Receipt Postcard

Respectfully submitted,

MCDERMOTT, WILL & EMERY

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